

CLAIMS

1. Device for generating a descrambling signal, comprising a first generator providing a descrambling base signal, a second generator providing a watermark signal, and means for combining the descrambling base signal and the watermark signal into a descrambling signal, wherein the watermark signal generated by the second generator includes a device identification.

2. Device according to claim 1, wherein the watermark signal generator comprises a pseudo random sequence generator seeded by a key, a device identification sequence source, and a modulator, wherein the device identification sequence provided is modulated on the pseudo random sequence to obtain the watermark signal.

3. Device according to claim 2, wherein the modulator provides an exclusive or operation on the device identification sequence and the pseudo random sequence, wherein preferably the bit rate of the pseudo random sequence is much higher than the bit rate of the device identification sequence.

4. Device according to claim 2 ~~or 3~~, wherein the key delivered to the pseudo random sequence generator is received from an outside source.

5. Device ~~according to any one of the preceding claims~~, comprising a generator for generating a compressing hindering signal and means for inserting the hindering signal into the descrambling signal.

6. Device ~~according to any one of the preceding claims~~, comprising a pseudo random sequence generator, a device identification sequence source, and a modulator, wherein the device identification sequence provided is modulated on the pseudo random sequence to obtain the watermark signal, wherein the phase relationship between the pseudo random sequence and the device identification sequence is randomly selected.

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7. Device according to ^{Claim 1} ~~any one of claims 1-5~~, com-

prising, a pseudo random sequence generator, a device identification sequence source, and a modulator, wherein the device identification sequence provided is modulated on the
5 pseudo random sequence to obtain the watermark signal, wherein the device identification sequence is repetitively modulated on the pseudo random sequence, wherein at each repetition a control unit checks a next bit of the device
10 identification sequence and inverts the bits of the device identification sequence if this bit has a given logic value.

8. Device for generating a watermark signal, comprising a pseudo random sequence generator, a device identification sequence source, and a modulator, wherein the device identification sequence provided is modulated on the
15 pseudo random sequence to obtain the watermark signal, wherein the phase relationship between the pseudo random sequence and the device identification sequence is randomly selected.

9. Device for generating a watermark signal, comprising a pseudo random sequence generator, a device identification sequence source, and a modulator, wherein the device identification sequence provided is modulated on the
20 pseudo random sequence to obtain the watermark signal, wherein the device identification sequence is repetitively modulated on the pseudo random sequence, wherein at each
25 repetition a control unit checks a next bit of the device identification sequence and inverts the bits of the device identification sequence if this bit has a given logic value.

10. Device ^{Claim 1} ~~according to any one of the preceding~~
30 ~~claims~~, wherein the device is implemented in a secure device, such as a smart card.

11. System to detect a watermark signal hidden in an information signal, comprising a pseudo random signal generator, means for synchronising the pseudo random signal
35 generator and the information signal, means for detecting a data sequence hidden in the information signal and for determining the number (n) of watermark signals in the hidden data sequence and means for selecting every n^{th} bit from the

detected hidden data sequence as bits of one of the n watermark signals.

12. System according to claim 11, wherein said means for determining the number (n) of watermark signals comprises means for detecting the bit rate of the hidden data sequence and comparing the detected bit rate with the known bit rate of one watermark signal.